HEWLETT-PACKARD
JOURNAL
INDEX

Volume 46 January 1995 through December 1995

Part 1: Chronological Index

February 1995

Broadband Frequency Characterization of Optical Receivers Using Intensity Noise, *Douglas M. Baney and Wayne V. Sorin*

1.55-µm Fiber-Optic Amplifier

Erbium-Doped Fiber Amplifier Test System, *Edgar Leckel, Jürgen Sang, Rolf Müller, Clemens Rück, and Christian Hentschel*

Multi-Quantum-Well Ridge Waveguide Lasers for Tunable External-Cavity Sources, *Tirumala R. Ranganath, Michael J. Ludowise, Patricia A. Beck, Dennis J. Derickson, William H. Perez, Tim L. Bagwell, and David M. Braun*

Measurement of Polarization-Mode Dispersion, *Brian L. Heffner and Paul R. Hernday*

Jones Calculus

The Poincaré Sphere

The HP 8509A/B Lightwave Polarization Analyzer

A New Design Approach for a Programmable Optical Attenuator, *Siegmar Schmidt and Halmo Fischer*

Precision Reflectometer with Spurious-Free Enhanced Sensitivity, *David M. Braun, Dennis J. Derickson, Luis M. Fernandez, and Greg D. LeCheminant*

High-Power, Low-Internal-Reflection, Edge Emitting Light-Emitting Diodes, *Dennis J. Derickson, Patricia A. Beck, Tim L. Bagwell, David M. Braun, Julie E. Fouquet, Forrest G. Kellert, Michael J. Ludowise, William H. Perez, Tirumala R. Ranganath, Gary R. Trott, and Susan R. Sloan*

Jitter Analysis of High-Speed Digital Systems, *Christopher M. Miller and David J. McQuate*

Automation of Optical Time-Domain Reflectometry Measurements, *Frank A. Maier and Harald Seeger*

Design and Performance of a Narrowband VCO at 282 THz, *Peter R. Robrish, Christopher J. Madden, Rory L. VanTuyl, and William R. Trutna, Jr.*

Surface Emitting Laser for Multimode Data Link Applications, *Michael R.T. Tan, Kenneth H. Hahn, Yu-Min D. Houng, and Shih-Yuan Wang*

Generating Short-Wavelength Light Using a Vertical-Cavity Laser Structure, *Shigeru Nakagawa, Danny E. Mars, and Norihide Yamada*

A New, Flexible Sequencer Architecture for Testing Complex Serial Bit Streams, *Robert E. McAuliffe, James L. Benson, and Christopher B. Cain*

Shortening the Time to Volume Production of High-Performance Standard Cell ASICs, *Jay D. McDougal and William E. Young*

A Framework for Insight into the Impact of Interconnect on 0.35-mm VLSI Performance, *Prasad Raje*

Synthesis of 100% Delay Fault Testable Combinational Circuits by Cube Partitioning, *William K. Lam*

Better Models or Better Algorithms? Techniques to Improve Fault Diagnosis, *Robert C. Aitken and Peter C. Maxwell*

Bridging and Stuck-At Faults

Potential Detection

April 1995

A Low-Cost, High-Performance PA-RISC Workstation with Built-In Graphics, Multimedia, and Networking Capabilities, *Roger A. Pearson*

The PA 7100LC Microprocessor: A Case Study of IC Design Decisions in a Competitive Environment, *Mick Bass, Patrick Knebel, David W. Quint, and William L. Walker*

Design Methodologies for the PA 7100LC Microprocessor, *Mick Bass, Terry W. Blanchard, D. Douglas Josephson, Duncan Weir, and Daniel L. Halperin*

An I/O System on a Chip, *Thomas V. Spencer, Frank J. Lettang, Curtis R. McAllister, Anthony L. Riccio, Joseph F. Orth, and Brian K. Arnold*

An Integrated Graphics Accelerator for a Low-Cost Multimedia Workstation, *Paul Martin*

HP Color Recovery Technology, *Anthony C. Barkans*

True Color

Real-Time Software MPEG Video Decoder on Multimedia-Enhanced PA 7100LC Processors, *Ruby B. Lee, John P. Beck, Joel Lamb, and Kenneth E. Severson*

Overview of the Implementation of the PA 7100LC Multimedia Enhancements

HP TeleShare: Integrating Telephone Capabilities on a Computer Workstation, *S. Paul Tucker*

Caller-ID

Call Progress, DTMF Tones, and Tone Detection

Product Design of the Model 712 Workstation and External Peripherals, *Arlen L. Roesner*

Development of a Low-Cost, High-Performance, Multiuser Business Server System, *Dennis A. Bowers, Gerard M. Enkerlin, and Karen L. Murillo*

HP Distributed Smalltalk: A Tool for Developing Distributed Applications, *Eileen Keremitsis and Ian J. Fuller*

Object Management Group

A Software Solution Broker for Technical Consultants, *Manny Yousefi, Adel Ghoneimy, and Wulf Rehder*

HP Software Solution Broker Accessible Products

Bugs in Black and White: Imaging IC Logic Levels with Voltage Contrast, *Jack D. Benzel*

Component and System Level Design-for-Testability Features Implemented in a Family of Workstation Products, *Bulent I. Dervisoglu and Michael Ricchetti*

June 1995

Capillary Electrophoresis: A Product of Technological Fusion, *Robert R. Holloway*

A New High-Performance Capillary Electrophoresis Instrument, *Fred Strohmeier*

Capillary Electrophoresis Applications

HP CE Technology Transfer

Industrial Design of the HP CE Instrument

A High-Sensitivity Diode Array Detector for On-Column Detection in Capillary Electrophoresis, *Patrick Kaltenbach*

Capillary Handling in the HP Capillary Electrophoresis Instrument, *by Hans-Peter Zimmermann*

Rapid Prototyping for the HP CE Project

Sample Injection in HP CE, *Werner Schneider*

HP CE Separation Control Electronics and Firmware, *Fritz Bek, Franz Bertsch, and Klaus Witt*

A User Interface for Capillary Electrophoresis, *Alwin Ritzmann and Klaus Witt*

Development of a Common ChemStation Architecture

Reproducibility Testing of the HP CE Instrument, *Ulrike Jegle*

The Impact of Column Technology on Protein Analysis by Capillary Electrophoresis: Surface Coatings and Analytical Approaches for Assessment, *Sally A. Swedberg and Monika Dittmann*

A New High-Sensitivity Capillary Electrophoresis Detector Cell and Advanced Manufacturing Paradigm, *Gary B. Gordon, Richard P. Tella, and Henrique A.S. Martins*

HP Disk Array: Mass Storage Fault Tolerance for PC Servers, *Tom A. Skeie and Michael R. Rusnack*

An Overview of Raid Technology

COBOL SoftBench: An Open Integrated CASE Environment, *Cheryl Carmichael*

Development and Use of Electronic Schematic Capture in the Specification and Simulation of a Structured-Custom ASIC, *David A. Burgoon*

Design and Development of a 120-MHz Bus Interface Block Using Standard Cells and Automatic Place and Route Tools, *Robert E. Ryan*

August 1995

Introduction to 100VG-AnyLAN and the IEEE 802.12 Local Area Network Standard, *Alan R. Albrecht and Patricia A. Thaler*

Cable Types

Other Network Technologies

Demand Priority Protocol, *Alan R. Albrecht, Michael P. Spratt, Patricia A. Thaler, and Gregory C.A. Watson*

Network Protocol Layers

Physical Signaling in 100VG-AnyLAN, *Alistair N. Coles, David G. Cunningham, Joseph A. Curcio, Jr., Daniel J. Dove, and Steven G. Methley*

Cross Talk in Unshielded Twisted-Pair Cables

Multilevel Signaling

Cross Talk Analysis

Optical-Fiber Links for 100VG-AnyLAN

Coding in 100VG-AnyLAN, *Simon E.C. Crouch and Jonathan Jedwab*

IEEE 802.3 and 802.5 Frame Formats

Polynomial Arithmetic and Cyclic Redundancy Checks

Multimedia Applications and 100VG-AnyLAN, *John R. Grinham and Michael P. Spratt*

Remote Bridge Example

Higher-Level Protocols

Related Projects

100VG-AnyLAN 15-Port Hub Design, *Lisa S. Brown*

Invalid Packet Marker

HP AccuPage 2.0: A Toolkit for High-Quality Document Scanning, *Steven L. Webb, Steven G. Henry, Kevin S. Burke, and George Prokop*

An 11.8-in Flat Panel Display Monitor, *David J. Hodge, Bradly J. Foster, Steven J. Kommrusch, and Tom J. Searby*

Liquid Crystal Display Technology

Product Design of the HP S1010A Flat Panel Display

A Note About VRAMs

Applying an Improved Economic Model to Software Buy-versus-Build Decisions, *Wesley H. Higaki*

Benchmark Standards for ASIC Technology Evaluation, *Antonio A. Martinez, Aloke S. Bhandia, and Henry H.W. Lie*

October 1995

HP PE/SolidDesigner: Dynamic Modeling for Three-Dimensional Computer-Aided Design, *Klaus-Peter Fahlbusch and Thomas D. Roser*

User Interaction in HP PE/SolidDesigner, *Berthold Hug, Gerhard J.Walz, and Markus Kühl*

Enhancements in Blending Algorithms, *Stefan Freitag and Karsten Opitz*

Open Data Exchange with HP PE/SolidDesigner, *Peter J. Schild, Wolfgang Klemm, Gerhard J. Walz, and Hermann J. Ruess*

Providing CAD Object Management Services through a Base Class Library, *Claus Brod and Max R. Kublin*

Exception Handling and Development Support

Freeform Surface Modeling, *Michael Metzger and Sabine Eismann*

Common Lisp as an Embedded Extension Language, *Jens Kilian and Heinz-Peter Arndt*

Boolean Set Operations with Solid Models, *Peter H. Ernst*

Fighting Inaccuracies: Using Perturbation to Make Boolean Operations Robust

A Microwave Receiver for Wide-Bandwidth Signals, *Robert J. Armantrout*

Firmware Design for Wide-Bandwidth IF Support and Improved Measurement Speed

The HP 89400 Series Vector Signal Analyzers

An IF Module for Wide-Bandwidth Signals, *Robert J. Armantrout, Terrence R. Noe, Christopher E. Stewart, and Leonard M. Weber*

The Log Weighted Average for Measuring Printer Throughput, *John J. Cassidy, Jr.*

December 1995

DCE: An Environment for Secure Client/Server Computing, *Michael M. Kong*

Adopting DCE Technology for Developing Client/Server Applications, *Paul Lloyd and Samuel D. Horowitz*

DCE Directory Services, *Michael M. Kong and David Truong*

X/Open Federated Naming, *Elizabeth A. Martin*

HP Integrated Login, *Jane B. Marcus, Navaneet Kumar, and Lawrence J. Rose*

The DCE Security Service, *Frederic Gittler and Anne C. Hopkins*

An Evolution of DCE Authorization Services, *Deborah L. Caswell*

An Object-Oriented Application Framework for DCE-Based Systems, *Mihaela C. Gittler, Michael Z. Luo, and Luis M. Maldonado*

HP Encina/9000: Middleware for Constructing Transaction Processing Applications, *Pankaj Gupta*

Object-Oriented Perspective on Software System Testing in a Distributed Environment, *Mark C. Campbell, David K. Hinds, Ana V. Kapetanakis, Stephen J. McFarland, David S. Levin, David J. Miller, and J. Scott Southworth*

The Object Management Group's Distributed Object Model

Object-Oriented Programming

A New, Lightweight Fetal Telemetry System, *Andreas Boos, Michelle Houghton Jagger, Günter W. Paret, and Jürgen W. Hausmann*

Zero Bias Detector Diodes for the RF/ID Market, *Rolando R. Buted*

Backscatter RF/ID Systems

Part 2: Subject Index

Subject **Page/Month**

0-9

10Base-T ..... 6/Aug.
100Base-T ..... 10/Aug.
100VG-AnyLAN ..... 6/Aug.
100VG-AnyLAN hub ..... 39/Aug.
5B/6B coding scheme ..... 27/Aug.
70-MHz down-converter ..... 83, 98/Oct.
8/7 mode ..... 71/Apr.

A

Abort ..... 65/Dec.
Abort recovery ..... 65/Dec.
Abstract class ..... 60/Dec.
Abstract server ..... 55/Dec.
Access control list (ACL) ..... 49/Dec.
Access delay ..... 16/Aug.
Accuracy problem ..... 48/Oct.
ACID ..... 65/Dec.
ACIS kernel ..... 7/Oct.
ACL database ..... 50/Dec.
Action routines ..... 14/Oct.
Active tags ..... 94/Dec.
Adaptive threshold ..... 48/Aug.
Aggregates ..... 13/Dec.
AIM (advanced interconnect modeling) ..... 97/Feb.
Algebraic factorizations ..... 108/Feb.
Alias entries ..... 25/Dec.
Amplified spontaneous emission (ASE) ..... 7, 13/Feb.
Amplifier test set ..... 15/Feb.
Amplifier, 1.55-μm fiber-optic ..... 9/Feb.
Amplitude compensation circuit ..... 91/Oct.
Analog video ..... 52/Aug.
Analytic blends ..... 26/Oct.
Analytic surface ..... 25/Oct.
Analytic surface type detection ..... 66/Oct.
Antireflection coating ..... 25, 45, 68/Feb.
Apex creation ..... 32/Oct.
APIs ..... 24, 28, 29, 32, 42/Dec.
Application namespace ..... 23/Dec.
Application server ..... 69/Dec.
Approach values ..... 77/Oct.
Arbitration ..... 38/Apr.
Architectural model ..... 8/Aug.
ASIC design 91/Feb., 88, 92/June
ASIC DFT design rules ..... 107/Apr.
ASIC technology evaluation ..... 68/Aug.
ATM ..... 10/Aug.
Atomic name ..... 29/Dec.
Attenuator, optical ..... 34/Feb.
Attributes ..... 53/Oct., 24, 29/Dec.
Audio ..... 8/Apr.
Audio interface ..... 40/Apr.
Auditing ..... 47/Dec.
Auger recombination ..... 23/Feb.
Authenticated RPC ..... 11, 47/Dec.
Authentication protocol ..... 38, 43/Dec.
Authorization ..... 42, 49/Dec.
Automatic place and route 92/June
Automatic scaling ..... 55/Aug.
Automatic scan insertion ..... 94/Feb.
Automatic vehicle identification (AVI) ..... 95/Dec.
Autosampler 14, 30/June

B

Back porch ..... 52/Aug.
Backlight saver ..... 51/Aug.
Backscatter RF/ID ..... 95/Dec.
Bandwidth, LAN ..... 15/Aug.
Bandwidth allocators ..... 37/Aug.
Bandwidth guarantees ..... 35/Aug.
Bandwidth sharing ..... 36/Aug.
Base class ..... 60/Dec.
Base class library ..... 51/Oct.
Base development environment ..... 63/Dec.
Basic local operation (B-LOP) ..... 8/Oct.
Behavioral modeling ..... 91/Feb.
Behavioral simulation ..... 26/Apr.
Benchmark circuits ..... 67/Aug.
Bézier splines ..... 62/Oct.
B-frames ..... 62/Apr.
Binding ..... 29/Dec.
Birefringence ..... 28/Feb.
BIST implementation ..... 109/Apr.
Bit processor ..... 78/Feb.
Bit slice 93/June
Blank level ..... 53/Aug.
Blend surface creation ..... 29/Oct.
Blending ..... 24, 61/Oct.
Block codes ..... 27/Aug.
Block move ..... 44/Apr.
Block transfer ..... 43/Apr.
Block write ..... 48/Apr.
B-LOP (basic local operation) ..... 8/Oct.
Blue light ..... 72/Feb.
Boolean engine ..... 74/Oct.
Boolean factorizations ..... 108/Feb.
Boolean set operations ..... 7, 8, 74/Oct.
Boundary representation (B-rep) modelers ..... 7/Oct.
Bounded delay ..... 36/Aug.
Branch data architecture ..... 73/Dec.
B-Rep model ..... 74/Oct.
B-Rep (boundary representation) modelers ..... 7/Oct.
Bridging defects ..... 112/Feb.
Bridging fault model ..... 112/Feb.
B-spline ..... 25, 62/Oct.
B-tree clustered file ..... 67/Dec.
Bubble cell 14, 63/June
Bubble factor 14, 64/June
BubbleWorld 66/June
Buffer 6/June
Bulletin board ..... 59/Oct.
Burst error ..... 30/Aug.
Business servers ..... 79/Apr.
Bus interface block 92/June
Bus sizing 94/June

C

C SoftBench 82/June
C++ ..... 55/Dec.
C++ class library ..... 53/Dec.
C++ SoftBench 82/June

Cable types ..... 7/Aug.
Cache (Model 712) ..... 6/Apr.
Cache organization (PA 7100LC) .. 16/Apr.
CAD object manager ..... 51/Oct.
Calibration attenuator ..... 94/Oct.
Calibration. optical receiver ..... 6/Feb.
Call progress ..... 69, 73/Apr.
Canal surfaces ..... 26/Oct.
Caller-ID ..... 69, 72/Apr.
Capillary 6, 11, 25, 57/June
Capillary cooling system 27/June
Capillary electrophoresis 6/June
Capillary electrophoresis detector cell 62/June
Capillary electrophoresis instrument 10/June
Capillary gel electrophoresis 6/June
Capillary handling 25/June
Capillary temperature control 41/June
Capping ..... 68/Oct.
Capture window ..... 95/Dec.
Carrier lifetime ..... 90/Oct.
Carry lookahead adder ..... 66/Apr.
Cascaded control 42/June
Cascaded hubs ..... 14/Aug.
Cassette 25/June
Catch block ..... 60/Dec.
Catch information ..... 15/Oct.
CDS advertiser ..... 26/Dec.
CDS directory structure ..... 26/Dec.
Cell Directory Service (CDS) ..... 23/Dec.
Cell manager ..... 69/Dec.
Ceramic resonator ..... 92/Oct.
Channel filters ..... 83, 98/Oct.
Charge-coupled device (CCD) .... 45/Aug.
Child pointers ..... 26/Dec.
CHIPTEST instruction ..... 108/Apr.
Chiral analysis 12/June
Chunk size 78/June
Circuit domain ..... 97/Feb.
Class ..... 60/Dec.
Clearinghouses ..... 26/Dec.
Client classes ..... 55/Dec.
Client proxy object ..... 55/Dec.
Client/server binding ..... 10/Dec.
Client/server model ..... 6/Dec.
Clock tree synthesis and verification ..... 94/Feb.
Cluster manager ..... 51, 56/Oct.
COBOL SoftBench 82/June
CODEC ..... 71/Apr.
Coding in 100VG-AnyLAN ..... 27/Aug.
Cofactor ..... 106/Feb.
Color lookup tables ..... 49/Apr.
Color precision ..... 52/Apr.
Color theory ..... 52/Apr.
Command-line interface (CLI) .... 71/Dec.
Commit ..... 65/Dec.
Common ACL management module interface ..... 51/Dec.
Common Lisp ..... 63/Oct.
Common Object Request Broker Architecture (CORBA) 86, 95/Apr., 76/Dec.
Complex interfaces ..... 77/Feb.
Complex signals ..... 87/Oct.
Composite name ..... 30/Dec.
Composite video signals ..... 52/Aug.
Compound name ..... 29/Dec.
Computer vision 68/June
Concrete data class ..... 60/Dec.
Constant-radius blends ..... 25/Oct.
Constructive solid geometry (CSG) modelers ..... 7/Oct.
Constructors ..... 60/Dec.
Containment values ..... 77/Oct.
Context ..... 29/Dec.
Context implementation ..... 28/Dec.
Control signaling ..... 24/Aug.
Conversation or session keys ..... 43/Dec.
Corporate centralized data architecture ..... 73/Dec.
Cost-benefit analysis ..... 64/Aug.
CPU partitioning (PA 7100LC) .... 14/Apr.
Crash recovery ..... 65/Dec.
Critical path testing ..... 108/Feb.
Cross talk ..... 19, 21/Aug.
Cross talk analysis ..... 22/Aug.
Cross-tangent curves ..... 28/Oct.
Cryptographic keys ..... 41/Dec.
Cube ..... 106/Feb.
Cube partitioning ..... 105/Feb.
Current measurements 41/June
Cyclic redundancy checks ..... 31/Aug.

D

Data access arrangement ..... 69, 71/Apr.
Data encryption ..... 43/Dec.
Data encryption standard (DES) .. 43/Dec.
Data exchange, HP PE/SolidDesigner ..... 35/Oct.
Data structure manager ..... 51/Oct.
Database server ..... 61/Dec.
DCAM ..... 71/Dec.
DCE cell ..... 7, 68/Dec.
DCE directory services ..... 23/Dec.
DCE infrastructure ..... 19/Dec.
DCE names ..... 23/Dec.
DCE namespace ..... 13, 23/Dec.
DCE registry ..... 38/Dec.
DCE RPC ..... 8/Dec.
DCE security service ..... 41/Dec.
DCE threads ..... 7/Dec.
Dced ..... 44/Dec.
dpi (dots per inch) ..... 45/Aug.
Debug and test (PA 7100LC) ..... 32/Apr.
Debugging serial tests ..... 87/Feb.
Delay fault testable circuits ..... 105/Feb.
Delay modeling ..... 99/Feb.
Delay ratio ..... 103/Feb.
Delay versus wire layer ..... 102/Feb.
Demand priority protocol ..... 8, 13/Aug.
Derived class ..... 60/Dec.
Design methodologies (PA 7100LC) ..... 23/Apr.
Design process (PA 7100LC) ..... 13/Apr.
Design rules, ASIC DFT ..... 107/Apr.
Design rules, metal width ..... 101/Feb.
Design rules, I_{DDQ} testing ..... 33/Apr.
Design-for-testability features ... 107/Apr.
Desktop scanners ..... 43/Aug.
Destructors ..... 60/Dec.
Detection, CE 11/June
Detector diodes ..... 94/Dec.
Detector linearity error ..... 97/Oct.
Detector module 10/June
DFS client components ..... 14/Dec.
DFS server components ..... 14/Dec.
DFS token management ..... 14/Dec.
Differential group delay ..... 29/Feb.
Differential quantum efficiency ... 23/Feb.
Digital video standards ..... 60/Apr.
Dimension-driven modifications .. 12/Oct.
Diode array detector 20/June
Diode array spectrograph 11/June
Directory information base ... 24, 25/Dec.
Directory service interfaces ..... 24/Dec.
Directory services ..... 23/Dec.
Directory system agent (DSA) .... 25/Dec.
Directory user agent (DUA) ..... 25/Dec.
Discrete cosine transform ..... 60/Apr.
Disk array controller 71/June
Distance learning seminar ..... 38/Aug.
Distinguished name ..... 25/Dec.
Distributed computing application management (DCAM) ..... 71/Dec.
Distributed Computing Environment (DCE) ..... 6/Dec.
Distributed file service (DFS) .... 13/Dec.
Distributed time service (DTS) ... 12/Dec.
Distributed system testing ..... 76/Dec.
Dither ..... 52/Apr.
Dither table shape ..... 56/Apr.
DNA analysis 12/June
Document scanning ..... 43/Aug.
Dot clock regeneration ..... 53/Aug.
Double-balanced mixer ..... 99/Oct.
Driving values ..... 12/Oct.
DTMF tones ..... 73/Apr.
DTS clerks ..... 12/Dec.
DTS servers ..... 12/Dec.
DUA cache ..... 25/Dec.
Dual issue ..... 20/Apr.
Dual-tone multifrequency (DTMF) ..... 69/Apr.
Dynamic modeling ..... 6, 7/Oct.
Dynamic range 22/June

E

Economic model ..... 61/Aug.
Edge emitting LED (EELED) ..... 43/Feb.
Edge nonmanifold ..... 75/Oct.
EISA-to-SCSI controller 71/June
EISAConfig 76/June
Electrodes 26/June
Electrokinetic injection 15/June
Electronic schematic capture 88/June

Electroosmotic flow 6/June
Electrophoresis 6/June
Embedded clocks ..... 77/Feb.
EMC and EMI control ..... 10/Apr.
Encapsulation ..... 95/Apr.
Encina/9000 toolkit ..... 62/Dec.
Encina cell ..... 68/Dec.
End-to-end delay ..... 34/Aug.
Enterprise namespace ..... 23/Dec.
Entity manager ..... 51, 53/Oct.
Entry-sequenced file ..... 67/Dec.
Erbium-doped fiber amplifier (EDFA) ..... 9/Feb.
Erbium-doped fiber amplifier test system ..... 13/Feb.
Error detection ..... 27/Aug.
Ethernet (IEEE 802.3) ..... 6/Aug.
Euler operators ..... 25/Oct.
Evaluating performance claims ... 67/Aug.
Evaluation 43/June
Event notification ..... 88/Apr.
Exception handling ..... 55/Oct., 60/Dec.
Exception model ..... 58/Dec.
Extended lightpath capillary 14/June
Extended privilege attribute certificate (EPAC) ..... 42/Dec.
Extended registry attribute (ERA) ..... 42, 45/Dec.
Extension language ..... 69/Oct.

F

Failing test vectors ..... 110/Feb.
Failure notification 80/June
Failure recovery 80/June
Fallback security technology ..... 36/Dec.
Far-end cross talk (FEXT) ..... 19/Aug.
Fast text ..... 44/Apr.
Fault diagnosis ..... 110/Feb.
Fault models ..... 111/Feb.
FDDI ..... 10/Aug.
Feature recognition ..... 10/Oct.
Federated naming ..... 28/Dec.
Fetal monitoring measuring methods ..... 83/Dec.
Fetal telemetry system ..... 82/Dec.
Fiber test system, automatic ..... 57/Feb.
Field programmable gate arrays (FPGAs) ..... 30/Apr.
Filesets ..... 13/Dec.
Filleting ..... 25/Oct.
Filter function logic, ... 57/Apr.
Filter pole gain ..... 95/Oct.
Finger information ..... 35/Dec.
Firmware, CE 36/June
Fixed analyzer method ..... 30/Feb.
Flat panel display ..... 51/Aug.
Floating-point unit ..... 19/Apr.
Flushing 32/June
FM demodulator ..... 82/Oct.
FM discriminator ..... 83, 98/Oct.
FM outputs ..... 98/Oct.
Forward differencing ..... 61/Apr.
Frame buffer ..... 43/Apr.
Frame buffer architecture ..... 56/Aug.
Frame buffer control ..... 58/Aug.
Frame formats ..... 30/Aug.
Frame rate matching ..... 56/Aug.
Frame rate synchronization ..... 59/Aug.
Framing algorithms ..... 78/Feb.
Framework classes ..... 56/Dec.
Free solution capillary electrophoresis (FSCE) 6/June
Freeform blends ..... 26/Oct.
Freeform surfaces ..... 61/Oct.
Frequency response, optical receivers ..... 6/Feb.
Fringing ..... 100/Feb.
Front porch ..... 52/Aug.
Frontal analysis 59/June
f_T (transition frequency) ..... 97/Oct.
Fused silica CE capillary 6, 57/June
Fusion method ..... 99/Apr.

G

GDS components ..... 25/Dec.
Generic model, serial communication systems ..... 78/Feb.
Generic Security Service API .. 42, 47/Dec.
Geometry ..... 76/Oct.
Geometry engine ..... 61/Oct.
Geometry selection ..... 10/Oct.
Global Directory Agent (GDA) .... 23/Dec.
Global Directory Service (GDS) ... 23/Dec.
Global namespace ..... 23/Dec.
Graphics accelerator ..... 43/Apr.
Graphics chip ..... 43/Apr.
GSC (general system connect) bus ..... 6, 80/Apr.
GSC interface ..... 36/Apr.
Guaranteed performance ..... 15/Aug.

H

Halfword instructions ..... 64/Apr.
Hardware caches ..... 84/Oct.
Hardware description language (HDL) ..... 91/Feb.
Hardware RAID architectures 73/June
HCRX-8 graphics device ..... 52/Apr.
Hierarchical bit streams ..... 77/Feb.
High priority ..... 13/Aug.
High-voltage control 40/June
High-Q resonators ..... 91/Oct.
Histograms and thresholds ..... 48/Aug.
History-based systems ..... 7/Oct.
HIVE ..... 97/Feb.
Hot-plug 71/June
HP AccuPage ..... 43/Aug.
HP CE instrument 10/June
HP ChemStation 10, 44/June
HP Color Recovery ..... 49, 51/Apr.
HP Distributed Smalltalk ..... 85, 95/Apr.
HP-PAC ..... 76/Apr.
HP-PB bus converter ..... 80/Apr.
HSYNC ..... 52/Aug.
Hub design ..... 39/Aug.
Hubs ..... 13/Aug.

I

I_{DDQ} implementation ..... 33/Apr.
IDL compiler (idl++) ..... 55/Dec.
IEEE 802.12 ..... 6/Aug.
IETF ..... 36/Aug.
IF bandwidth filter ..... 94/Oct.
IF module ..... 82, 89/Oct.
I-frames ..... 61/Apr.
IGES ..... 38/Oct.
I-LOP (intelligent local operation) .. 8/Oct.
Imaging, voltage contrast ..... 102/Apr.
Impulse noise ..... 21/Aug.
In-circuit emulation ..... 30/Apr.
Index guided laser ..... 20/Feb.
Indexed color ..... 52/Apr.
Information technology ..... 16/Dec.
Inheritance ..... 97/Apr.
Injection 32/June
Injection control algorithm 41/June
Integrated login ..... 34/Dec.
Integrated login library (libauth library) ..... 35/Dec.
Intelligent local operation (I-LOP) .. 8/Oct.
Intensity ..... 45/Aug.
Intensity noise technique ..... 6/Feb.
Interarrival-time jitter ..... 35/Aug.
Interconnect ..... 97/Feb.
Interface Definition Language (IDL) ..... 85/Apr., 6, 55/Dec.
Interface repository ..... 87/Apr.
Interferometric method ..... 30/Feb.
Interline capacitance ..... 100/Feb.
Internal sampling ..... 34/Apr.
Interpolation profiles ..... 63/Oct.
Interpolation subtraction method ..... 13/Feb.
Interrogator ..... 94/Dec.
Interrupt controller ..... 39/Apr.
Intersection graph ..... 75/Oct.
Intracoded frames ..... 61/Apr.
I-Q down-converter ..... 83, 102/Oct.
Isoelectric focusing 6/June
Isotachophoresis 6/June
Iterative prototyping ..... 99/Apr.
ITU-T narrowband ISDN ..... 36/Aug.

J

Japanese ID code ..... 89/Dec.
Jitter analysis ..... 49/Feb.
Jitter tolerance ..... 55/Feb.
Jitter transfer ..... 55/Feb.
Jones calculus ..... 28/Feb.
Jones matrix ..... 27/Feb.
JPEG ..... 60/Apr.
JTAG (IEEE 1149.1) ..... 42/Apr.
Junctions ..... 26/Dec.

K

Kerberos ..... 38, 41/Dec.
Key distribution center (KDC) .... 45/Dec.
Keys ..... 43/Dec.

L

LAN megacell ..... 41/Apr.
Laser narrowband ..... 63/Feb.
Laser, blue ..... 72/Feb.
Laser, multi-quantum well ridge waveguide ..... 20/Feb.
Laser, surface emitting ..... 67/Feb.
Laser, vertical-cavity ..... 72/Feb.
LASI (LAN/SCSI) chip ..... 6, 36, 80/Apr.
Legacy environment ..... 16/Dec.
Libauth library ..... 35/Dec.
Lifetime, session ..... 36/Dec.
Linear accuracy ..... 48/Oct.
Linear detector circuit ..... 96/Oct.
Link initialization ..... 15/Aug.
Liquid crystal display (LCD) ..... 51/Aug.
Liquid crystal display technology . 53/Aug.
Liquid-phase analysis 8/June
Local area network technology .... 6/Aug.
Local operations ..... 8/Oct.
Locking service ..... 64/Dec.
Lofting ..... 61/Oct.
Log weighted average ..... 104/Oct.
Logic synthesis ..... 91/Feb.
Logical channel identification .... 79/Feb.
Logical data volumes ..... 64/Dec.
Login ..... 34/Dec.
Login expirations ..... 37/Dec.
LOPs ..... 8/Oct.

M

Malan/Wentzel model ..... 61/Aug.
Manifold solids ..... 74/Oct.
Marching algorithm ..... 26/Oct.
Master/slave replication ..... 72/Dec.
Match criteria ..... 73/Apr.
Media access control (MAC) ..... 18/Aug.
Media recovery ..... 65/Dec.
Member functions ..... 60/Dec.
Memory controller (PA 7100LC) .. 15/Apr.
Message digest 5 (MD5) algorithm 43/Dec.
Metal migration analysis 94/June
Method execution 37/June
Micellar electrokinetic chromotography 6/June
Microglass lathe 63/June
Microlathe 66/June
Microwave receiver ..... 80/Oct.
Middleware ..... 61/Dec.
Mirroring ..... 72/Dec.
Migration time reproducibility 51/June
Mixed-language debugging 86/June
Mixed-language model 86/June
Modeling resolution ..... 78/Oct.
Modular measurement system (MMS) ..... 81/Oct.
Movement control 40/June
MPEG compression ..... 61/Apr.
MPEG decoding ..... 65/Apr.
MPEG decompression ..... 63/Apr.
MPEG-1 ..... 60/Apr.
MPower 2.0 ..... 60/Apr.
MQUAD ..... 15/Apr.
Multicell configurations ..... 47/Dec.
Multilevel signaling ..... 21/Aug.
Multilevel synthesis ..... 107/Feb.
Multimedia applications ..... 33/Aug.
Multimedia instructions ..... 21/Apr.
Multimedia workstation ..... 6/Apr.
Multiple interfaces ..... 77/Feb.
Multiplexing ..... 29/Aug.
Multithreaded processes ..... 69/Dec.
Multi-quantum-well ridge waveguide lasers ..... 20/Feb.

N

Name ..... 23, 29/Dec.
Name-based authorization ..... 42/Dec.
Name service independent (NSI) API ..... 11, 24/Dec.
Namespace ..... 23, 29/Dec.
Naming ..... 88/Apr.
Naming convention ..... 29/Dec.
Naming federation ..... 28/Dec.
Naming policy ..... 28/Dec.
Naming service ..... 28, 29/Dec.
Naming syntax ..... 28/Dec.
Naming systems ..... 23, 28, 29/Dec.
Narrowband laser ..... 63/Feb.
Nd:YVO_4 ..... 63/Feb.
Near-end cross talk (NEXT) ..... 19/Aug.
Nested transaction ..... 65/Dec.
Net present value ..... 62/Aug.
Network Computing Architecture (NCA) ..... 8/Dec.
Network Computing System (NCS) . 8/Dec.
Network data representation (NDR) ..... 10/Dec.
Network interface card ..... 41/Aug.
Network protocol layers ..... 15/Aug.
Network time protocol (NTP) .... 12/Dec.
Network topology ..... 8/Aug.
Next-naming-system pointer ..... 30/Dec.
Node manager ..... 69/Dec.
Noise 22/June
Nondeterministic bit streams ..... 77/Feb.
Normal priority ..... 13/Aug.
NURBS (nonuniform rational B-spline) ..... 25, 61/Oct.

O

Object ..... 60/Dec.
Object class ..... 24/Dec.
Object entries ..... 26/Dec.
Object identifier ..... 25/Dec.
Object management group (OMG) ..... 85, 86/Apr., 76/Dec.
Object management services ..... 51/Oct.
Object-oriented application framework ..... 55/Dec.
Object-oriented development environment ..... 85/Apr.
Object-oriented programming .... 79/Dec.
Object request broker (ORB) ..... 85/Apr., 76/Dec.
Object technology ..... 97/Apr.
Object testing framework (OTF) ..... 75, 77/Dec.
OCR algorithms ..... 46/Aug.
OCR engine ..... 47/Aug.
Offset surface ..... 27/Oct.
Oligonucleotide failure sequences .. 8/June
OMVPE ..... 22, 44/Feb.
OODBMS ..... 87/Apr.
OODCE ..... 55/Dec.
Optical attenuator ..... 34/Feb.
Optical character recognition (OCR) ..... 43/Aug.
Optical connector endface characterization ..... 40/Feb.
Optical isolator characterization ..... 41/Feb.
Optical low-coherence reflectometry ..... 43/Feb.
Optical receiver characterization ..... 6/Feb.
Optical sampling rate ..... 45/Aug.
Optical system 34/Apr., 20/June
Optical time-domain Reflectometry ..... 57/Feb.
ORB (object request broker) ..... 85/Apr., 76/Dec.
Order fulfillment ..... 83/Apr.
Organic acid analysis 12/June
OTDR system ..... 57/Feb.
Override file ..... 38/Dec.

P

Paced byte wide ..... 37/Apr.
Paced word wide ..... 37/Apr.
Packed byte wide ..... 37/Apr.
Page mode ..... 47/Apr.
Page segmentation ..... 47/Aug.
Parallel port ..... 39/Apr.
Parallelism ..... 77/Oct.
Parasolid ..... 7/Oct.
PA-RISC multimedia instructions ..... 64/Apr.
PA-RISC PA 7100LC processor ..... 6, 12, 23/Apr.
Partial Booleans ..... 77/Oct.
Passive tags ..... 94/Dec.
Passwords ..... 34/Dec.
Password strength checking algorithms ..... 37/Dec.
PDES Inc. ..... 45/Oct.
Peak area reproducibility 51/June
Peak purity measurement 13/June
Peer-to-peer communication ..... 67/Dec.
Peptide mapping 8/June
Perl ..... 70/Dec.
Personality ..... 18, 21/Oct.

Personality modules ..... 82/Feb.
Perturbation ..... 78/Oct.
P-frames ..... 62/Apr.
Photoreceiver characterization .... 6/Feb.
Physical layer (PHY) ..... 18/Aug.
Physical medium dependent (PMD) sublayer ..... 18/Aug.
Physical medium independent (PMI) sublayer ..... 18/Aug.
Physical specifications ..... 78/Feb.
P-i-n diodes ..... 93/Oct.
Pin grid array ..... 14/Apr.
Pinned dialog boxes ..... 15/Oct.
PLA methodology ..... 23/Apr.
Place and route 93/Feb., 92/June
Poincaré sphere ..... 27, 29/Feb.
Poiseuille flow 7/June
Polarization dispersion vector .... 28/Feb.
Polarization extinction method ... 14/Feb.
Polarization-mode dispersion ..... 27, 41/Feb.
Policy chain ..... 36/Dec.
Polyethylene glycol surface coatings 59/June
Polymorphism ..... 97/Apr.
Polynomial arithmetic ..... 31/Aug.
Portable Operating System Interface (POSIX) ..... 7/Dec.
Postsilicon verification .... 26, 29, 31/Apr.
Potential detection ..... 115/Feb.
ppi (pixels per inch) ..... 45/Aug.
Precision reflectometer ..... 39/Feb.
Prepare ..... 65/Dec.
Preselector centering ..... 85/Oct.
Presentation/semantic split ... 88, 97/Apr.
Presilicon verification ..... 26, 28/Apr.
Pressure injection 15/June
Pressure measurements 41/June
Primary login ..... 35/Dec.
Primary storage 72/June
Principal ..... 42/Dec.
Principal keys ..... 43/Dec.
Printer throughput ..... 104/Oct.
Privilege-based authorization ..... 42/Dec.
Privilege service ..... 45/Dec.
PRODEX ..... 45/Oct.
Product design (Model 712) ..... 75/Apr.
Product design (HP S1010A) ..... 57/Aug.
Product stewardship ..... 77/Apr.
Program animation 85/June
Programmable gain block ..... 95/Oct.
Programmable optical attenuator ..... 34/Feb.
ProSTEP ..... 45/Oct.
Protein analysis 57/June
Protocol data unit (PDU) ..... 10/Dec.
Pseudo color ..... 52/Apr.
Pulse detection ..... 96/Oct.

Q

Q ..... 89/Oct.
Quad flat pack (QFP) ..... 14/Apr.
Quantum well ..... 21, 68/Feb.
Quartet signaling ..... 20/Aug.

R

Radio frequency identification (RF/ID) ..... 94/Dec.
RAID technology .... 71, 74/June, 72/Dec.
Railroad ..... 20/Oct.
Random testing ..... 29/Apr.
Rapid prototyping 29/June
RC delay ..... 101/Feb.
Reader ..... 94/Dec.
Read-only tags ..... 94/Dec.
Read/write tags ..... 94/Dec.
Real-time clock ..... 39/Apr.
Receiver characterization, optical ..... 6/Feb.
Receiver, microwave ..... 80/Oct.
Recoverable queueing service .... 68/Dec.
Rectangular area fill ..... 44/Apr.
Refcount entities ..... 53/Oct.
Reference ..... 29/Dec.
Reflection of solids ..... 78/Oct.
Reflectometer, precision optical .. 39/Feb.
Reflectometry, optical low-coherence, ... 43/Feb.
Regional centralized data architecture ..... 73/Dec.
Registry ..... 37, 41/Dec.
Relation solver ..... 7, 13/Oct.
Relations ..... 53/Oct.
Relative distinguished name ..... 25/Dec.
Relative file ..... 67/Dec.
Relative intensity noise (RIN) ..... 8/Feb.
Relaxing ..... 25/Oct.
Remote bridge ..... 35/Aug.
Remote procedure call (RPC) ..... 86/Apr., 8/Dec.
Remote procedure call (RPC) interfaces ..... 28/Dec.
Repeater chip ..... 40/Aug.
Replenishment control 41/June
Replenishment system 14, 34/June
Reproducibility testing 50/June
Request window ..... 14/Aug.
Resolution ..... 45/Aug.
RF module ..... 81/Oct.
Ridge laser ..... 23/Feb.
Ridge structure ..... 43/Feb.
Ridge waveguide laser ..... 20/Feb.
RLC tank circuit ..... 91/Oct.
Robustly path delay fault testable (RPDFT) ..... 105/Feb.
Rolling ball blend ..... 25/Oct.
Romulus ..... 7/Oct.
Root of a namespace ..... 23/Dec.
Root hub ..... 14/Aug.
Round-robin node selection ..... 13/Aug.
Rounding ..... 24, 25/Oct.
RPC NSI API ..... 11, 24/Dec.
RPC protocols ..... 10/Dec.
RPC server entries ..... 26/Dec.

S

Sample and electrolyte handling .. 29/June
Sample injection 32/June
Saturation arithmetic ..... 65/Apr.
Saturation logic ..... 66/Apr.
Scaling ..... 49, 55/Aug.
Scanner software ..... 43/Aug.
SCSI megacell ..... 41/Apr.
Second-harmonic generation ..... 72/Feb.
Secondary storage 72/June
Secret key ..... 41/Dec.
Secure communication ..... 45/Dec.
Security ..... 90/Apr., 34/Dec.
Security server ..... 41/Dec.
Security technologies ..... 34/Dec.
Semantic/presentation split ... 88, 97/Apr.
Sensitivity 22/June
Sensitivity gain, bubble cell 65/June
Separation control 36/June
Separation environment 11/June
Separation principle 6/June
Separation subunit 38/June
Separation unit 10/June
Sequencer architecture ..... 76/Feb.
Serial device test sequencer ..... 76/Feb.
Serial test card ..... 76/Feb.
Serial Test Language ..... 76, 85/Feb.
Serial test sequencer ..... 82/Feb.
Server classes ..... 55/Dec.
Service ticket ..... 42/Dec.
Servos 39/June
Sheet ..... 75/Oct.
Short-wavelength laser ..... 72/Feb.
Simple average ..... 104/Oct.
Simple dithering ..... 52/Apr.
Simple weighted average ..... 104/Oct.
Single sign-on problem ..... 39/Dec.
Single-step login ..... 34/Dec.
Singularity ..... 28, 32/Oct.
Sinusoidal jitter ..... 53/Feb.
Skinning ..... 68/Oct.
Small point-size characters ..... 46/Aug.
Smalltalk ..... 85, 94/Apr.
Smooth operator ..... 106/Feb.
Soft links ..... 26/Dec.
Software buy-versus-build decisions ..... 61/Aug.
Software caches ..... 85/Oct.
Software RAID architectures 73/June
Software Solution Broker ..... 93/Apr.
Software testing ..... 75/Dec.
Software video support ..... 48/Apr.
Solid modeling design system ..... 6/Oct.
Source code control 83/June
Spectral library 12/June
Spectrum analyzer ..... 80/Oct.
Spider diagram ..... 81/Apr.
Spine ..... 27/Oct.
Spline ..... 62/Oct.
Spline library ..... 64/Oct.

Spurious responses, EELED ..... 44/Feb.
Standard cells 91/Feb., 92/June
State manager ..... 58/Oct.
Step gains ..... 95/Oct.
STEP (standard for exchange of product model data) ..... 43/Oct.
Stop register/split transfer ..... 47/Apr.
Stray light 22/June
Stripe set 77/June
Stuck-at-faults ..... 110/Feb.
Structured file system ..... 67/Dec.
Subcontext ..... 29/Dec.
Superscalar execution ..... 20/Apr.
Surface coatings 57/June
Surface emitting laser ..... 67/Feb.
Surface modeling ..... 61/Oct.
Surface modified capillaries 58/June
Sweeping ..... 68/Oct.
Switching ..... 10/Aug.
Symbol synchronization algorithms ..... 78/Feb.
Synchronously tuned filters ..... 89/Oct.
Synthesis and routing ..... 23/Apr.
System administration ..... 70/Dec.
System administration manager (SAM) ..... 71/Dec.

T

Tag ..... 94/Dec.
Tangential intersections ..... 28, 32/Oct.
TAP/SAP controller ..... 108/Apr.
Target transmission time ..... 37/Aug.
Technological domain ..... 97/Feb.
Technology constant ..... 67/Aug.
Telemetry receiver ..... 90/Dec.
Telemetry transmitter ..... 85/Dec.
Telephony ..... 9/Apr.
Teleservices project ..... 38/Aug.
Temperature control 38/June
Test developers ..... 76/Dec.
Test harness ..... 75/Dec.
Test management subsystem ..... 78/Dec.
Test system, EDFA ..... 13/Feb.
Test system, optical fiber ..... 57/Feb.
TestCase class ..... 78/Dec.
TestEnvironment ..... 77/Dec.
Testing complex serial bit streams ..... 76/Feb.
Thread-to-TID ..... 63/Dec.
Threads ..... 7/Dec.
Three-tiered architectures ..... 62/Dec.
Threshold ..... 45/Aug.
Throw statement ..... 60/Dec.
Ticket-granting ticket (TGT) ..... 42/Dec.
Time-delay discriminator ..... 99/Oct.
TLB ..... 18/Apr.
TLB (translation lookaside buffer) ..... 18, 79/Apr.
TMIn ..... 23/Feb.
TM-XA ..... 64/Dec.
TOCO driver ..... 88/Dec.
Token ring (IEEE 802.5) ..... 6/Aug.
Tone detection ..... 73/Apr.
Tool body ..... 7, 8/Oct.
Topology ..... 25, 29, 63, 76/Oct.
Transaction(s) ..... 58/Oct., 65/Dec.
Transaction manager ..... 63/Dec.
Transaction processing ..... 61/Dec.
Transaction processing monitor ..... 68/Dec.
Transactional RPC ..... 63/Dec.
Transceiver chip ..... 41/Aug.
Transformation algorithms ..... 79/Feb.
Transition curves ..... 31/Oct.
Transition frequency (f_T) ..... 97/Oct.
Transponder ..... 94, 95/Dec.
Trimmed mode ..... 39/Oct.
Trimmed parametric mode ..... 40/Oct.
Trimming ..... 28/Oct.
Tryptic digest analysis 12/June
True color ..... 52/Apr.
Try block ..... 60/Dec.
Tunable laser sources ..... 20/Feb.
Two-level circuit ..... 106/Feb.
Two-phase locking ..... 65/Dec.
Two-tiered architecture ..... 61/Dec.

U

Unbiased rounding ..... 64/Apr.
Undo operations ..... 59/Oct.
Unforgeable identities ..... 50/Dec.
Universal unique identifier (UUID) ..... 9, 23, 41/Dec.
Unpaced byte wide ..... 37/Apr.
Unpaced word wide ..... 37/Apr.
Unshielded twisted pair (UTP) ..... 6/Aug.
Untrimmed mode ..... 39/Oct.
Update handlers ..... 59/Oct.
Upgrading existing networks ..... 10/Aug.
User interface builder ..... 22/Oct.
User interface, capillary electrophoresis 44/June
User interface, HP PE/SolidDesigner ..... 14/Oct.
Uterine activity measurement ..... 88/Dec.
Utility classes ..... 56/Dec.
UV/Vis absorption detection 11/June

V

Varactor diodes ..... 93/Oct.
Variable-bandwidth design ..... 89/Oct.
Variable-radius blends ..... 25/Oct.
VCO, 282 THz ..... 63/Feb.
Vector signal analysis ..... 83/Oct.
Vector signal analyzers ..... 87/Oct.
Verification methodology ..... 25/Apr.
Vertex nonmanifold ..... 75/Oct.
Vertex regions ..... 25, 31/Oct.
Vertical-cavity laser ..... 72/Feb.
Vertical retrace ..... 52/Aug.
Video standards ..... 60/Apr.
Virtual functions ..... 60/Dec.
Virtual shelf ..... 93/Apr.
Vision, computer 68/June
VisualWorks ..... 93/Apr.
Voice mode operation ..... 72/Apr.
Voltage contrast imaging ..... 102/Apr.
Voltage, current, or power control 40/June
Voltage measurement 40/June
Voltage sensitivity ..... 96/Dec.
VRAMs ..... 43/Apr., 56, 59/Aug.
VSYNC ..... 52/Aug.

W

Wall ..... 102/Apr.
Wavelength scanning method ..... 30/Feb.
Window accelerator ..... 43/Apr.
Wire capacitance ..... 100/Feb.
Wire geometry ..... 98/Feb.
Wire load models ..... 92/Feb.
Wireframe mode ..... 41/Oct.
Workplane set ..... 64/Oct.
Workstation, multimedia ..... 6/Apr.
Write-ahead logging ..... 63, 65/Dec.

XYZ

XBAR ..... 69, 70/Apr.
X.500 ..... 25/Dec.
XDS/XOM APIs ..... 24/Dec.
XFN API ..... 29/Dec.
XFN enterprise policies ..... 32/Dec.
XFN protocols ..... 30/Dec.
X/Open Federated Naming (XFN) ..... 28/Dec.
YCbCr ..... 62/Apr.
YIG-tuned filter (YTF) ..... 81/Oct.
Zero bias diode ..... 94/Dec.
Zero injection effect 52/June
Zipping ..... 26/Oct.